



Feature list for AURA enabled BSP for QNX 6.3.x

CPU: Power Architecture Technology, 32-bit

AURA is a preemptive and reentrant hardware abstraction layer (HAL). AURA's unique API design allows for context switch free calls to HAL functions. Use of the AURA BSP can remove hardware dependencies. An AURA enabled QNX boot image will function as intended on any AURA-supported hardware. AURA compounds software development investment.

- Applications can be moved immediately from one AURA-supported platform to another - hardware migration is easier and faster.
- AURA can also be used to add new features to existing products - making forward compatibility more manageable in less time.

Supported CPU types:

603, 604, G3 (750...), G4 (74xx, 86xx)

Kernel-based functions:

- Memory initialization
- Full hardware device recognition
- Interrupt handling
- Power management handling
- Debug device (failsafe console)

Userland functions

- Time/date/RTC support
- PCI server
- Vendor independent framebuffer support for embedded or PCI based graphics

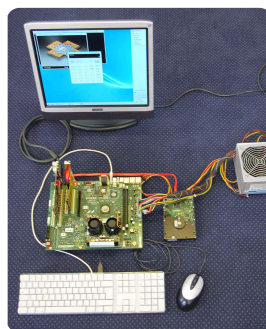
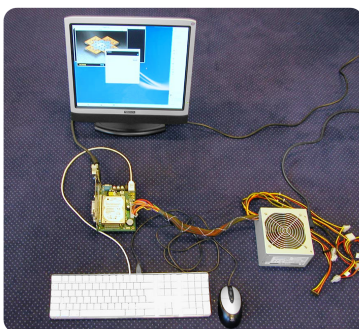
Board-specific functions (example)

EFIKA (currently MPC5200B based) device drivers that are dynamically configured and supported:

- Ethernet 10/100
- Serial port
- AC97 (record/play, SPDIF)
- IDE
- USB (ships with QNX 6.3.x itself)

Other AURA-supported boards

Various Power 32-bit platforms including Marvell Discovery II / III / V, Tundra 108 and Freescale MPC86xx based boards. Board-specific functions are device dependent and available on request.



Images

Left - MPC5200B based EFIKA running AURA enabled QNX

Right - Dual G4 Marvell DB64560 running the same AURA enabled QNX